



TRIMON 250

High-Performance Multi-Threaded Processor Core

Feature Specification

- High-Performance MIPS I Instruction Set Compatible Processor Core
- Extensions and accelerators to the Instruction Set can be added
- 16kB Instruction Memory (IMEM). IMEM size can be changed based on application requirements.
- 16kB Data Memory (DMEM) DMEM size can be changed based on application requirements.
- 250MHz Operating frequency in 0.18um TSMC process
- Operating frequency can be increased if necessary.
- 0.3mm**2 in 0.18 TSMC process without memories
- 1.5mm**2 in 0.18 TSMC process including memories
- Support for 3 external independent memory systems, memory load-store units included.
- The External Memory Interface is scalable and it can support different partitioning.
- Gaskets can be added to support buses like AMBA etc.
- RS232 based software debugger (optional)
- Ideal for SoC data-communication, packet processors, packet classification engines and embedded applications.

The TRIMON 250 IP Package

- TRIMON 250 Verilog RTL
- Verilog Testbench
- Verification Environment, scripts and tools
- Assembler development toolchain
- "C" development toolchain
- Micro-Kernel program management firmware

Block Diagram

